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Docket No.: 60188-075

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Makoto FUJIWARA

Serial No.: 09/867,766

Filed: May 31, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING
SEMICONDUCTOR INTEGRATED CIRCUIT

: Customer Number: 20277

: Confirmation Number: 5700

: Group Art Unit: 2133

: Examiner: not yet assigned

INFORMATION DISCLOSURE STATEMENT

RECEIVED

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Commissioner for Patents
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Alexandria, VA 22313-1450

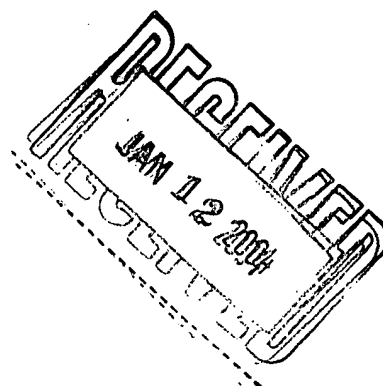
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Technology Center 2100

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

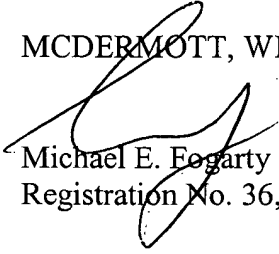


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Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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| INFORMATION DISCLOSURE CITATION IN AN APPLICATION | | | | | | ATTY. DOCKET NO. 60188-075 | | SERIAL NO. 09/867,766 | | | |
| (PTO-1449) | | | | | | APPLICANT Makoto FUJIWARA | | | | | |
| | | | | | | FILING DATE May 31, 2001 | | GROUP 2133 | | | |
| U.S. PATENT DOCUMENTS | | | | | | | | | | | |
| EXAMINER'S INITIALS | | CITE NO. | Document Number Number-Kind Code ₂ (if known) | | Publication Date MM-DD-YYYY | | Name of Patentee or Applicant of Cited Document | | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | | |
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